

FIG. 1 is a schematic diagram of a broadcast TV schedule display. The display is divided into several sections. At the top, there are two rows of labels: "CHL # CALL LETTERS" (labeled 202 and 204) and a row of time slots (labeled 208) including "5:00 am", "5:30 am", "6:00 am", "6:30 am", "7:00 am", and "7:30 am". Below these are two rows of placeholder text: "XXX XXXX" and "XXXXXXX XXXX XX". The main body of the display is titled "BROADCAST TV SCHEDULE" and contains a grid of asterisks (*) representing scheduled programs. The grid has four rows of asterisks, followed by several rows of dashed lines indicating further scheduling information.

The diagram illustrates a network architecture. On the left, a computer (300) is shown. A line (302) connects the computer to an oval labeled 'INTERNET' (304). Another line (306) connects the 'INTERNET' oval to a rectangular box labeled 'PROVIDING COMPUTER' (306). The entire diagram is enclosed in a dashed rectangular border.

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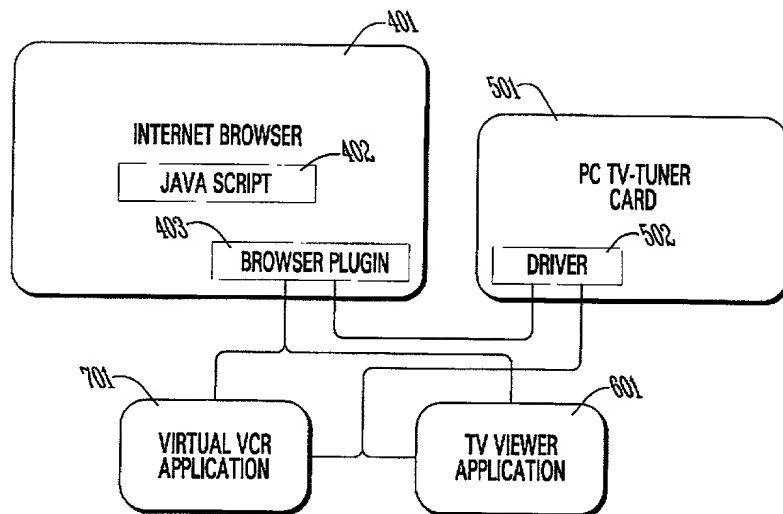


Fig. 4